

10Gb/s XFP Optical Transceiver Module

SXP3102ZX-M

(10GBASE-ZR, 1550nm, EML, APD)

1. INTRODUCTION

This document is a specification for a single channel XFP MSA transceiver module. The transceiver shall be IEEE802.3ae 10GBASE-ER compliant. The transceiver is a bi-directional device with a transmitter and receiver in a same package. The following describes common features and ability:

- XFP MSA compliant mechanical platform
- Up to 11.1Gbps rate applications
- 1550nm range laser transmitter with automatic output power control
- InGaAs-APD photo-detector receiver
- LC-duplex receptacle optical connector
- +3.3V and +5.0V power supply
- Commercial operating temperature range -5 to +85°C
- No reference clock required
- Built-in EEPROM with digital diagnostic monitoring function
- Hot pluggable XFP compatible footprint

Notice: This document has been created by Sumitomo Electric Industries, Ltd. (SEI). SEI reserves the rights to at any time to add, amend, or withdraw statements contained herein.

2. MAXIMUM AND OPERATING OPTICAL AND ELECTRICAL REQUIREMENTS

2.1. ABSOLUTE MAXIMUM RATINGS

The maximum ratings below indicate the stress over which the transceiver may be irrecoverably damaged.

Parameter	Symbol	Condition	Min	Max	Unit	Note
Storage Temperature	Tstg		-40	+85	°C	
Storage Humidity	Hst	continuous	5	85	%	*1
Storage Humidity	Hst	short-term	5	95	%	*2
Operating Case Temperature	Tcase		-5	+85	°C	*3
Supply Voltage	Vcc5		-0.3	+5.5	V	
Supply Voltage	Vcc3		-0.3	+3.63	V	

*1: Non-condensing.

*2: Short-term refers to a period of not more than 96 consecutive hours and a total of not more than 15 days in 1 year.

*3: Actual temperature, does not integrate monitor accuracy margin.

2.2. OPERATING CONDITIONS

The operating conditions below indicate the conditions under which the transceiver shall operate normally and meet the performance specification.

Parameter	Symbol	Condition	Min	Max	Unit	Note
Operational Humidity	RH		5	85	%	*1
Operating Case Temperature	Tcase	continuous	0	+85	°C	*2
Supply Voltage	Vcc5		+4.75	+5.25	V	
Supply Voltage	Vcc3		+3.13	+3.47	V	

*1: Non-condensing.

*2: Actual temperature, does not integrate monitor accuracy margin.

3. FUNCTIONAL DESCRIPTION

All performance in this chapter shall be specified under operating conditions described in chapter 2.2 unless otherwise specified.

3.1. OPTICAL CHARACTERISTICS

Parameter	Symbol	Condition	Min (*1)		Typ	Max (*1)		Unit	Note
			non-FEC rate	FEC rate		non-FEC rate	FEC rate		
TRANSMITTER									
Signaling Speed			9.95/10.3/10.7/11.1					Gbps	
Output Average Power	Pout		0			+4		dBm	
Wavelength	Lc_tx		1530			1565		nm	
Dynamic Extinction Ratio	EX		9	8.2				dB	
Side-mode Suppression Ratio	Sr		30					dB	
Jitter Generation	J_tx	4MHz-80MHz	Compliant with GR-253					UIpp	
Eye Mask Margin	MM		10					%	*2
Average Launch Power off OFF Transmitter						-30		dBm	
OPTICAL PATH									
Attenuation			11	11		22	20	dB	
Chromatic Dispersion	D _{LR}		0			1600	1400	ps/nm	
RECEIVER									
Signaling Speed			9.95/10.3/10.7/11.1					Gbps	
Sensitivity	Pin					-24	-23	dBm	*3,*4
Overload	Pol		-7					dBm	*3
Wavelength	Lc_rx		1530			1565		nm	
Rx Reflectance						-27		dB	
Optical Path Penalty	Pd_rx					2	3	dB	
Rx_LOS assert	LOSa					-33		dBm	
Rx_LOS de-assert	LOSd					-30		dBm	

*1: non-FEC rate refers 9.9/10.3Gbps, and FEC rate refers 10.7/11.1Gbps

*2: 9.95328Gbps for ITU-T mask, and 10.3125Gbps for IEEE mask

*3: BER of 1x10E-12 for non-FEC rate, and 1x10E-12 for FEC rate

*4: Source type EML

Transmitter Eye Mask Definition

Compliant with ITU-T G.691 STM-64 mask.

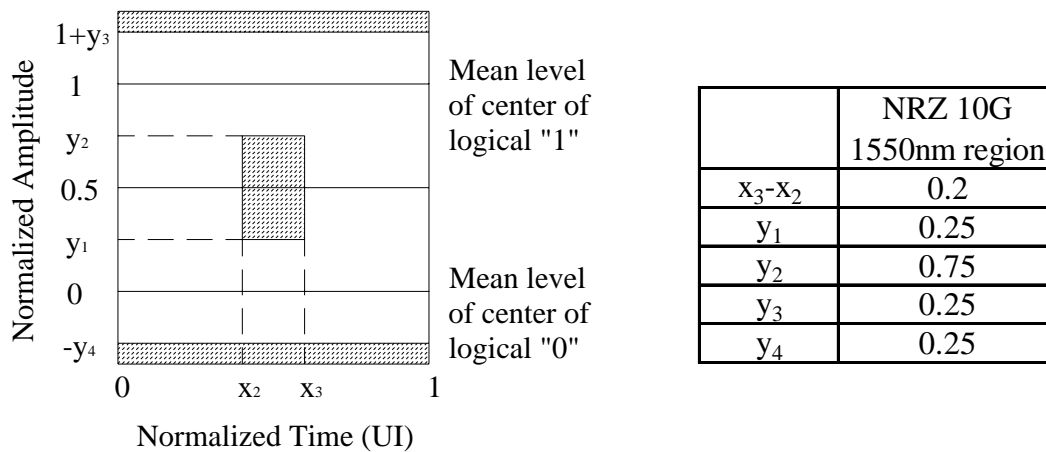


Fig. 1.1 Optical output eye mask (10Gbps)

Compliant with IEEE802.3ae Specifications.

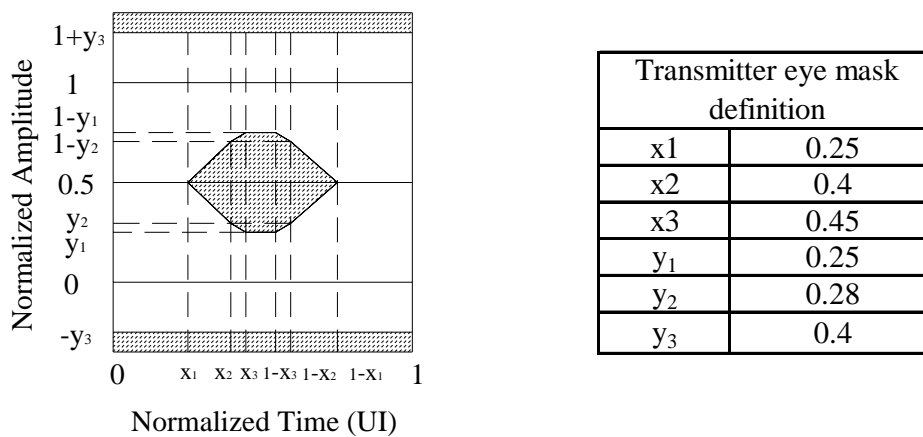


Fig. 1.2 Optical output eye mask (10Gbps)

3.2. ELECTRICAL CHARACTERISTICS

Parameter	Symbol	Condition	Min	Typ	Max	Unit	Note
TRANSMITTER							
Data Rate		NRZ		9.95/10.3/10.7/11.1		Gbps	
Input Single-ended Data Swing	V _{in}		60		410	mV _{pp}	
Input Differential Impedance	Z _d			100		Ω	
RECEIVER							
Data Rate		NRZ		9.95/10.3/10.7/11.1		Gbps	
Output Single-ended Data Swing	V _{out}		170		425	mV _{pp}	
Rise/Fall Time	tr/TF	20% - 80%	24			psec	
Low speed control and sense signals							
XFP Interrupt, Mod_NR, RX_LOS	V _{OL}		0		0.4	V	
	V _{OH}	host_Vcc-0.5			host_Vcc+0.3	V	
XFP TX_Dis, P_Down/RST	V _{IL}		-0.3		0.8	V	
	V _{IH}		2.0		Vcc3+0.3	V	
XFP SCL and SDA	V _{OL}		0		0.4	V	
	V _{OH}	host_Vcc-0.5			host_Vcc+0.3	V	
XFP SCL and SDA	V _{IL}		-0.3		Vcc3x0.3	V	
	V _{IH}	Vcc3x0.7			Vcc3+0.5	V	
Leakage Current	I _l		-10		10	μA	
Capacitance for XFP SCL and SDA I/O Pin	C _i				14	pF	
Total bus capacitive load for SCL and for SDA	C _b				100	pF	*1
					400	pF	*2
DC AND POWER SUPPLY							
Power Dissipation	P				3.5	W	
	P	P_Down mode			1.5	W	
Supply Voltage	Vcc5		4.75	5	5.25	V	
	Vcc3		3.13	3.3	3.47	V	
Supply Current	Icc5	Include inrush			500	mA	
	Icc3	Include inrush			750	mA	

*1: At 400 kHz, 3.0 kohms Rpullup, max.

*2: At 400 kHz, 0.8 kohms Rpullup, max.

Timing Requirements of Control and Status I/O

Parameter	Symbol	Min	Max	Unit	Condition	Note
TX Disable Assert Time	t _{off}		10	μsec	rising edge of TX_DIS to fall of output signal below 10% of nominal	
TX Disable Negate Time (cold-start)			120	sec	Falling edge of TX_DIS to rise of output signal above 90% of nominal	*1
TX Disable Negate Time (warm-start)	t _{on}		2	msec	Falling edge of TX_DIS to rise of output signal above 90% of nominal	*2
Time to initialize	t _{init}		300	msec	From power on or hot plug after supply power on host or from falling edge of P _{Down} /RST.	
Interrupt assert delay	Interrupt _{on}		200	msec	From occurrence of the condition triggering Interrupt	
Interrupt negate delay	Interrupt _{off}		500	μsec	From clear on read Interrupt flags	
P _{Down} /RST assert delay	P _{Down} /RST _{on}		100	μsec	From Power down initiation	
Mod _{NR} assert delay	Mod _{nr} _{on}		1	msec	From Occurrence of fault to assertion of MOD NR	
Mod _{NR} negate delay	Mod _{nr} _{off}		1	msec	From clearance of signal to negation of MOD NR	
P-Down reset time		10		μsec	Min length of P-Down assert to initiate	
RX _{LOS} assert delay	t _{loss} _{on}		100	μsec	From Occurrence of loss of signal to assertion of RX _{LOS}	
RX _{LOS} negate delay	t _{loss} _{off}		100	μsec	From Occurrence of presence of signal to negation of RX _{LOS}	

*1: Cold-start time is the time for the module to reach its steady state operating condition after a power-up.

*2: The warm-start time is measured from when the TX_DISABLE is negated on the module.

3.3. MANAGEMENT INTERFACE

Each module has unique data in hatching area.

A0h

Address	Size	Name of field	Description of Field	R/W	Hex	NOTE	
Lower Memory Map							
0	00	1	Identifier	XFP	R	06	
1	01	1			R,R/W	00	
2	02	2	Temp High Alarm	(+90degC)	R	5A	90
3	03			Units defined in MSA (1/256deg.C).	R	00	
4	04	2	Temp Low Alarm	(-10degC)	R	F6	-10
5	05				R	00	
6	06	2	Temp High Warning	(+88degC)	R	58	88
7	07				R	00	
8	08	2	Temp Low Warning	(-8degC)	R	F8	-8
9	09				R	00	
10	0A	8	Reserved A/D Flag Thresholds	Reserved A/D Flag Thresholds	R	00	
11	0B				R	00	
12	0C				R	00	
13	0D				R	00	
14	0E				R	00	
15	0F				R	00	
16	10				R	00	
17	11				R	00	
18	12	2	Bias High Alarm	(131mA)	R	FF	131
19	13			Units defined in MSA (2uA)	R	DC	
20	14	2	Bias Low Alarm	(1mA)	R	01	1
21	15				R	F4	
22	16	2	Bias High Warning	(78mA)	R	98	78
23	17				R	58	
24	18	2	Bias Low Warning	(18mA)	R	23	18
25	19				R	28	
26	1A	2	TX Power High Alarm	(+7.0dBm)	R	C3	7.00
27	1B			Units defined in MSA (0.1uW)	R	C6	
28	1C	2	TX Power Low Alarm	(-3.0dBm)	R	13	-3.00
29	1D				R	93	
30	1E	2	TX Power High Warning	(+5.0dBm)	R	7B	5.00
31	1F				R	86	
32	20	2	TX Power Low Warning	(-1.0dBm)	R	1F	-1.00
33	21				R	07	
34	22	2	RX Power High Alarm	(-6.0dBm)	R	09	-6.00
35	23			Units defined in MSA (0.1uW)	R	CF	
36	24	2	RX Power Low Alarm	(-27.0dBm)	R	00	-27.21
37	25				R	13	
38	26	2	RX Power High Warning	(-8.0dBm)	R	06	-8.00
39	27				R	30	
40	28	2	RX Power Low Warning	(-25.0dBm)	R	00	-25.09
41	29				R	1F	
42	2A	2	AUX 1 High Alarm	(not supported)	R	00	
43	2B				R	00	
44	2C	2	AUX 1 Low Alarm	(not supported)	R	00	
45	2D				R	00	
46	2E	2	AUX 1 High Warning	(not supported)	R	00	
47	2F				R	00	
48	30	2	AUX 1 Low Warning	(not supported)	R	00	
49	31				R	00	
50	32	2	AUX 2 High Alarm	(not supported)	R	00	
51	33				R	00	
52	34	2	AUX 2 Low Alarm	(not supported)	R	00	
53	35				R	00	
54	36	2	AUX 2 High Warning	(not supported)	R	00	
55	37				R	00	
56	38	2	AUX 2 Low Warning	(not supported)	R	00	
57	39				R	00	
58	3A	2	VPS Fields	(not supported)	R	00	
59	3B				R	00	

A0h (continued)

60	3C	10	Reserved	(reserved)	R	00	
61	3D				R	00	
62	3E				R	00	
63	3F				R	00	
64	40				R	00	
65	41				R	00	
66	42				R	00	
67	43				R	00	
68	44				R	00	
69	45				R	00	
70	46	1	Acceptable BER	(not supported)	R	00	
71	47	1	Actual BER	(not supported)	R	00	
72	48	2	Wavelength Set MSB	(not supported)	R	00	
73	49				R	00	
74	4A	2	Wavelength Error LSB	(not supported)	R	00	
75	4B				R	00	
76	4C		Amplitude Adjustment	(not supported)	R	00	
77	4D		Phase Adjustment	(not supported)	R	00	
78	4E		Reserved	(not supported)	R	00	
79	4F				R	00	
80	50	8	Latched Interrupt Flag Fields	Latched on flag condition. Cleared on host read	R		
81	51				R		
82	52				R		
83	53				R		
84	54				R		
85	55				R		
86	56				R		
87	57				R		
88	58	8	Interrupt Masking Bits	Set and readable by host. Cleared at power up or reset	R/W	00	
89	59				R/W	00	
90	5A				R/W	00	
91	5B				R/W	00	
92	5C				R/W	00	
93	5D				R/W	00	
94	5E				R/W	00	
95	5F				R/W	00	
96	60	2	Temperature	Temperature value in units defined in MSA (1/256deg.C).	R		
97	61				R		
98	62	2	Reserved	(reserved)	R	00	
99	63				R	00	
100	64	2	TX Bias	Tx bias value in units defined in MSA (2uA)	R		
101	65				R		
102	66	2	TX Power	Tx power value in units defined in MSA (0.1uW)	R		
103	67				R		
104	68	2	RX Power	Rx power value in units defined in MSA (0.1uW)	R		
105	69				R		
106	6A	2	AUX 1	(not supported)	R	00	
107	6B				R	00	
108	6C	2	AUX 2	(not supported)	R	00	
109	6D				R	00	
110	6E	2	State	TX DIS/MOD_NR/P_Down/Interrupt et TX_NR/TX_Fault/TX_CDR etc	R,R/W		
111	6F				R		
112	70	6	Reserved	(reserved)	R	00	
113	71				R	00	
114	72				R	00	
115	73				R	00	
116	74				R	00	
117	75				R	00	
118	76	1	Packet Error Checking	01h =Enable Packet Error Checking	R/W	00	
119	77	4	New Password Entry	Location of Entry of New Optional Password	W		
120	78				W		
121	79				W		
122	7A				W		
123	7B	4	Password Entry	Location for Entry of Optional Password	W		
124	7C				W		
125	7D				W		
126	7E				W		
127	7F	1	Table Select	Entry Location for Table Select Byte	W		

Table 01h

Address	Size	Name of field	Description of Field	R/W	Hex	NOTE
Serial ID Data Fields						
128	80	1	Identifier	XFP	R	06
129	81	1	Ext.Identifier	3.5W, w_CDR, wo_Refclk, wo_CLEI	R	90
130	82	1	Connector	LC Connector	R	07
131	83	8	Transceiver codes	10GbE: not supported	R	00
132	84			10G FC: not supported	R	00
133	85			10G Copper: not supported	R	00
134	86			Lower Speed: not supported	R	00
135	87			SONET (SR): not supported	R	00
136	88			SONET (IR): not supported	R	00
137	89			SONET (LR): LR-2	R	00
138	8A			SONET (VLR): not supported	R	00
139	8B	1	Encoding	64B/66B, NRZ (10010000)	R	90
140	8C	1	BR-Min	9.9Gb/s	R	63 9.9
141	8D	1	BR-Max	11.1Gbps	R	6F 11.1
142	8E	1	Length(SMF)-km	80km	R	50 80
143	8F	1	Length (E-50 μm)		R	00
144	90	1	Length (50 μm)		R	00
145	91	1	Length (62.5 μm)		R	00
146	92	1	Length (Copper)		R	00
147	93	1	Device Technology	1550nm cooled EML, APD	R	76
148	94	16	Vendor name (ASCII code)		R	53 S
149	95				R	75 u
150	96				R	6D m
151	97				R	69 i
152	98				R	74 t
153	99				R	6F o
154	9A				R	6D m
155	9B				R	6F o
156	9C				R	45 E
157	9D				R	6C l
158	9E				R	65 e
159	9F				R	63 c
160	A0				R	74 t
161	A1				R	72 r
162	A2				R	69 i
163	A3				R	63 c
164	A4	1	CDR Support	11.1Gbps, XFI Loopback	R	F9
165	A5	3	Vendor OUI	IEEE Company Identifier of SEI	R	00
166	A6				R	00
167	A7				R	5F
168	A8	16	Vendor PN (ASCII code)	(e.g. SXP3102LV)	R	53 S
169	A9				R	58 X
170	AA				R	50 P
171	AB				R	33 3
172	AC				R	31 1
173	AD				R	30 0
174	AE				R	32 2
175	AF				R	5A Z
176	B0				R	58 X
177	B1				R	2D -
178	B2				R	4D M
179	B3				R	20
180	B4				R	20
181	B5				R	20
182	B6				R	20
183	B7				R	20
184	B8	2	Vendor rev (ASCII code)	(revision of datasheet)	R	41 A
185	B9				R	30 0
186	BA	2	Wavelength	(1547.5nm) Units defined in MSA (0.05nm)	R	78 1547.5
187	BB				R	E6
188	BC	2	Wavelength Tolerance	(17.5nm) Units defined in MSA (0.005nm)	R	0D 17.5
189	BD				R	AC
190	BE	1	Max Case Temp	(+85degC)	R	55 85
191	BF	1	CC_BASE	CC for the base ID fields (128-190)	R	
192	C0	4	Power Supply	Units defined in MSA (20mW)	R	96 3
193	C1				Units defined in MSA (10mW)	R
194	C2			Maximum current	R	A7
195	C3				R	00

Table 01h (continued)

196	C4	16	Vendor SN (ASCII code)	(e.g. 97H001100001)	R	39	9
197	C5				R	37	7
198	C6				R	48	H
199	C7				R	30	0
200	C8				R	30	0
201	C9				R	31	1
202	CA				R	31	1
203	CB				R	30	0
204	CC				R	30	0
205	CD				R	30	0
206	CE				R	30	0
207	CF				R	31	1
208	D0	R	20				
209	D1	R	20				
210	D2	R	20				
211	D3	R	20				
212	D4	8	Date code (ASCII code)	two low order digits of year. (00 =2000)	R	30	0
213	D5				R	39	9
214	D6			date code -month (01 -12)	R	30	0
215	D7				R	37	7
216	D8			date code-day (01 -31)	R	32	2
217	D9				R	33	3
218	DA		vendor specific lot code (ASCII)	R	30	0	
219	DB		R	31	1		
220	DC	1	Diagnostic Monitoring Typ	Average Power	R	08	
221	DD	1	Enhanced Options	Soft Tx Dis, Soft P Down	R	60	
222	DE	1	Aux Monitoring	(not supported)	R	00	
223	DF	1	CC_EXT	CC for the extended ID fields (192-222)	R		
224	E0	32	Vendor Specific	Vendor Specific ID Field	R	00	
225	E1				R	00	
226	E2				R	00	
227	E3				R	00	
228	E4				R	00	
229	E5				R	00	
230	E6				R	00	
231	E7				R	00	
232	E8				R	00	
233	E9				R	00	
234	EA				R	00	
235	EB				R	00	
236	EC				R	00	
237	ED				R	00	
238	EE				R	00	
239	EF				R	00	
240	F0				R	00	
241	F1				R	00	
242	F2				R	00	
243	F3				R	00	
244	F4				R	00	
245	F5				R	00	
246	F6				R	00	
247	F7				R	00	
248	F8				R	00	
249	F9				R	00	
250	FA				R	00	
251	FB				R	00	
252	FC				R	00	
253	FD				R	00	
254	FE				R	00	
255	FF				R	00	

Table 02h

Address	Size	Name of field	Description of Field	R/W	Hex	NOTE
User EEPROM Data						
128	80	128	User EEPROM Data	User EEPROM Data	R	00
129	81				R	00
130	82				R	00
131	83				R	00
132	84				R	00
133	85				R	00
134	86				R	00
135	87				R	00
136	88				R	00
137	89				R	00
138	8A				R	00
139	8B				R	00
140	8C				R	00
141	8D				R	00
142	8E				R	00
143	8F				R	00
144	90				R	00
145	91				R	00
146	92				R	00
147	93				R	00
148	94				R	00
149	95				R	00
150	96				R	00
151	97				R	00
152	98				R	00
153	99				R	00
154	9A				R	00
155	9B				R	00
156	9C				R	00
157	9D				R	00
158	9E				R	00
159	9F				R	00
160	A0				R	00
161	A1				R	00
162	A2				R	00
163	A3				R	00
164	A4				R	00
165	A5				R	00
166	A6				R	00
167	A7				R	00
168	A8				R	00
169	A9				R	00
170	AA				R	00
171	AB				R	00
172	AC				R	00
173	AD				R	00
174	AE				R	00
175	AF				R	00
176	B0				R	00
177	B1				R	00
178	B2				R	00
179	B3				R	00
180	B4				R	00
181	B5				R	00
182	B6				R	00
183	B7				R	00
184	B8				R	00
185	B9				R	00
186	BA				R	00
187	BB				R	00
188	BC				R	00
189	BD				R	00
190	BE				R	00

Table 02h (continued)

		(128)	(User EEPROM Data)	(User EEPROM Data)		
191	BF				R	00
192	C0				R	00
193	C1				R	00
194	C2				R	00
195	C3				R	00
196	C4				R	00
197	C5				R	00
198	C6				R	00
199	C7				R	00
200	C8				R	00
201	C9				R	00
202	CA				R	00
203	CB				R	00
204	CC				R	00
205	CD				R	00
206	CE				R	00
207	CF				R	00
208	D0				R	00
209	D1				R	00
210	D2				R	00
211	D3				R	00
212	D4				R	00
213	D5				R	00
214	D6				R	00
215	D7				R	00
216	D8				R	00
217	D9				R	00
218	DA				R	00
219	DB				R	00
220	DC				R	00
221	DD				R	00
222	DE				R	00
223	DF				R	00
224	E0				R	00
225	E1				R	00
226	E2				R	00
227	E3				R	00
228	E4				R	00
229	E5				R	00
230	E6				R	00
231	E7				R	00
232	E8				R	00
233	E9				R	00
234	EA				R	00
235	EB				R	00
236	EC				R	00
237	ED				R	00
238	EE				R	00
239	EF				R	00
240	F0				R	00
241	F1				R	00
242	F2				R	00
243	F3				R	00
244	F4				R	00
245	F5				R	00
246	F6				R	00
247	F7				R	00
248	F8				R	00
249	F9				R	00
250	FA				R	00
251	FB				R	00
252	FC				R	00
253	FD				R	00
254	FE				R	00
255	FF				R	00

All address of this table 02h is write enabled with valid password entry

4. MECHANICAL DESCRIPTION

XFP MSA compliant. Color coding is "White" and indicated at latch portion.

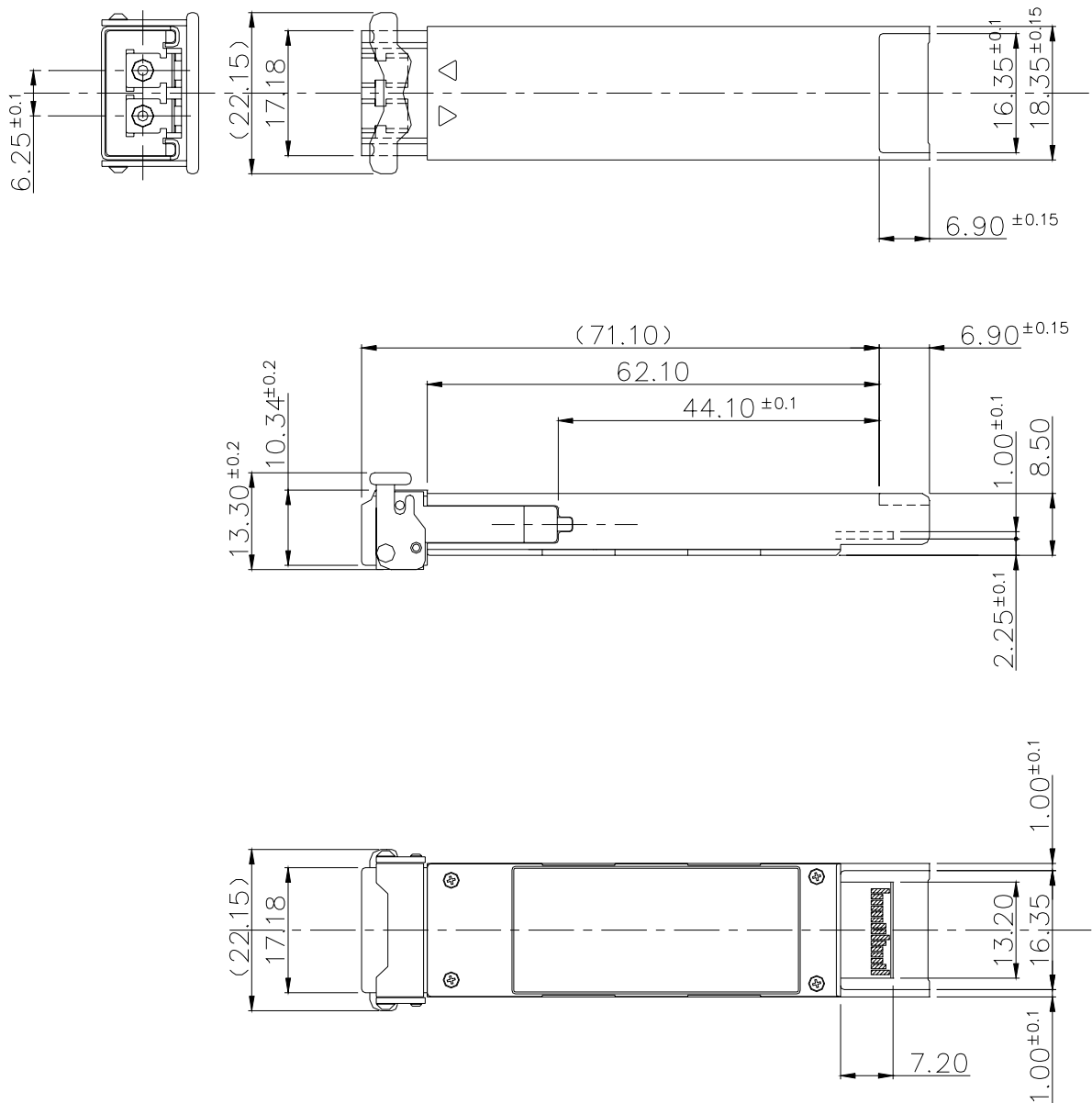


Fig. 2.1 Mechanical drawing of XFP

Laser Emission

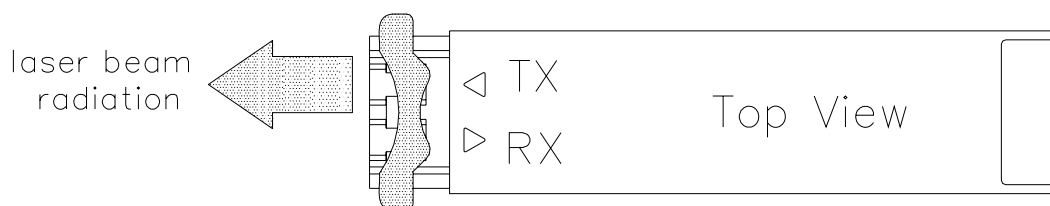


Fig. 2.2 Aperture and direction of laser beam radiation

5. PIN FUNCTION

Pin	Symbol	I/O	Use	Logic	Note
1	GND	-	Module Ground	NA	*1
2	VEE5	-	Optional -5.2V Power Supply	NA	*2
3	Mod_Dessel	I	Module De-select	LVTTTL	*3
4	Interrupt	O	Interrupt	LVTTTL	*4,*5
5	TX_DIS	I	Transmitter Disable	NA	
6	VCC5	-	+5V Power Supply	NA	
7	GND	-	Module Ground	NA	*1
8	VCC3	-	+3.3V Power Supply	NA	
9	VCC3	-	+3.3V Power Supply	NA	
10	SCL	I/O	2-Wire Serial Interface Clock	LVTTTL	*5
11	SDA	I/O	2-Wire Serial Interface Data Line	LVTTTL	*5
12	Mod_Abs	O	Indicates Module is not present. Grounded in the Module	LVTTTL	*5
13	Mod_NR	O	Module Not Ready; Indicating Module Operational Fault	LVTTTL	*5
14	RX_LOS	O	Receiver Loss Of Signal Indicator	LVTTTL	*5
15	GND	-	Module Ground	NA	*1
16	GND	-	Module Ground	NA	*1
17	RD-	O	Receiver Inverted Data Output	CML	
18	RD+	O	Receiver Non-Inverted Data Output	CML	
19	GND	-	Module Ground	NA	*1
20	VCC2	-	+1.8V Power Supply	NA	*2
21	P_DOWN/RST	I	Power down / Reset	LVTTTL	*6,*7
22	VCC2	-	+1.8V Power Supply	NA	*2
23	GND	-	Module Ground	NA	*1
24	RefCLK+	I	Reference Clock Non-Inverted Input, AC coupled on the host board	PECL	*8
25	RefCLK-	I	Reference Clock Inverted Input, AC coupled on the host board	PECL	*8
26	GND	-	Module Ground	NA	*1
27	GND	-	Module Ground	NA	*1
28	TD-	I	Transmitter Inverted Data Input	CML	
29	TD+	I	Transmitter Non-Inverted Data Input	CML	
30	GND	-	Module Ground	NA	*1

*1: Module ground pins Gnd are isolated from the module case and chassis ground within the module.

*2: Not used (internally left open)

*3: Module De-select; When held low allows module to respond to 2-wire serial interface.

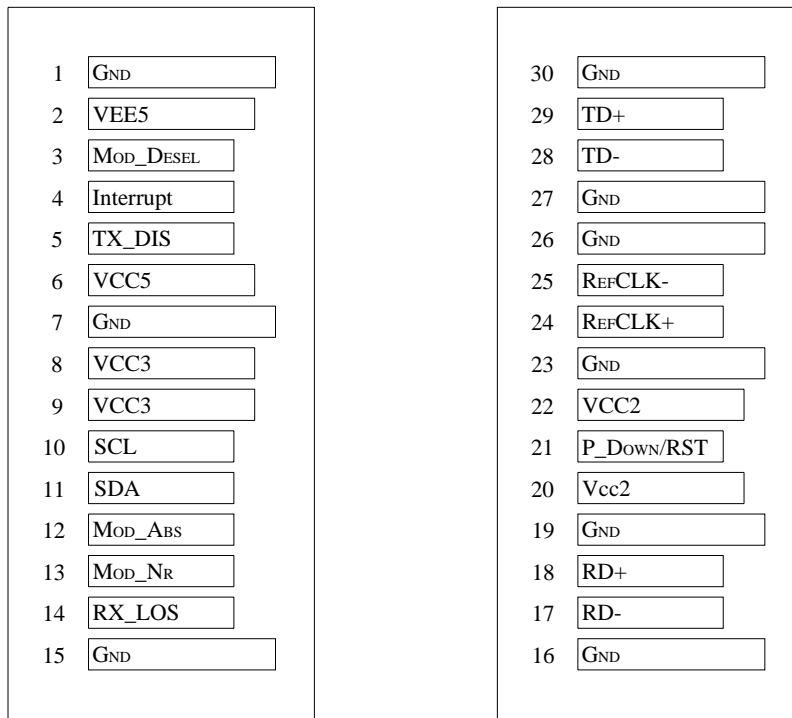
*4: Interrupt; Indicates presence of an important condition which can be read over the 2-wire serial interface.

*5: Shall be pulled up with 4.7K-10Kohms to a voltage between 3.15V and 3.45V on the host board.

*6: Power down; When high, requires the module to limit power consumption to 1.5W or below. 2-Wire serial interface must be functional in the low power mode.

*7: Reset; The falling edge initiates a complete reset of the module including the 2-wire serial interface, equivalent to a power cycle.

*8: Not used. Differential input impedance is 100Ω typ.



Bottom of Board
(As view through top of board)

Top of Board

Fig. 3 XFP Transceiver Electrical Pad Layout

6. LABEL

Example of standard XFP label layout



7. PACKING

Up to 5pcs transceivers shall be packed in a single box. Outer dimension of the box is 200 x 165 x 40mm (L x W x H). Fiber optic endcaps are individually installed on each transceivers.

8. REFERENCES

- 10 Gigabit Small Form Factor Pluggable (XFP) Transceiver Multi Source Agreement (MSA), April 2005
- ITU-T G.691 and Telcordia GR-253 Specifications
- IEEE802.3ae Specifications

9. HANDLING PRECAUTIONS

- The product is ESD sensitive device. Open and handle in a static-free environment only.
- The product is designed to be Class 1 and Class I laser compliant per IEC60825-1 and FDA/CDRH 21 CFR 1040 respectively. Do not look at laser beam direct exposure or its reflection while laser is on.
- Housing of the product during operation may be thermally hot and could cause personal injury.
- Clean connector endface of optical cable adequately before you make a connection.
- The product is unable to be aqueous washed. Do not wash the product in water, and not use this product near a wash bowl, sink or laundry, or in a wet place.

<End of Document>